

**AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph that begins on application page 11 at line 31 with the following replacement paragraph:

FIG. 2 shows a second embodiment of the capacitive detector, which partly uses a microprocessor 46 based architecture, suitable for use in the present invention. It operates in a corresponding manner as the capacitive detector described with reference to FIG. 1 and will therefore not be described again. An advantage with this second embodiment is that the output indicating a change in the electric field at the antenna 4 can be divided in several different levels.